

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10001477
	Filing Date		2001-11-01
	First Named Inventor	Steve Roe	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01203M	

## U.S. PATENTS

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1	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
2	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Monte Mar, filed on 04/25/2001; 28 pages	<input type="checkbox"/>

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1	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
2	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/28/2003; 6 pages	<input type="checkbox"/>
3	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
4	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>

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	1	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>

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1	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 51 pages	<input type="checkbox"/>
2	USPTO Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Warren Snyder, filed on 08/06/2001; 25 pages	<input type="checkbox"/>
3	USPTO Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 08/03/2001; 37 pages	<input type="checkbox"/>
4	USPTO Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller," Kutz et al., filed on 08/03/2001; 38 pages	<input type="checkbox"/>
5	USPTO Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable," James Shutt; filed on 07/24/2001; 33 pages	<input type="checkbox"/>
6	USPTO Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier," Kutz et al., filed on 06/26/2001; 22 pages	<input type="checkbox"/>
7	USPTO Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-Fly," Bert Sullam, filed on 04/02/2001; 24 pages	<input type="checkbox"/>
8	USPTO Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller," Kutz et al., filed on 06/22/2001; 42 pages	<input type="checkbox"/>
9	USPTO Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot," Nemecek et al., filed on 11/01/2001; 44 pages	<input type="checkbox"/>
10	USPTO Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer," Nemecek et al., filed on 11/01/2001; 47 pages	<input type="checkbox"/>
11	USPTO Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System," Craig Nemecek, filed on 11/01/2001; 43 pages	<input type="checkbox"/>

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12	USPTO Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," Nemecek et al., filed on 11/14/2001; 46 pages	<input type="checkbox"/>
13	USPTO Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control," Nemecek et al., filed on 11/14/2001; 47 pages	<input type="checkbox"/>
14	USPTO Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller," Sullam et al., filed on 10/24/2001; 34 pages	<input type="checkbox"/>
15	USPTO Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller," Kutz et al., filed on 06/22/2001; 44 pages	<input type="checkbox"/>
16	USPTO Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks," Warren Snyder, filed on 05/14/2001; 28 pages	<input type="checkbox"/>
17	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
18	USPTO Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture," Warren Snyder, filed on 10/15/2002; 36 pages	<input type="checkbox"/>
19	USPTO Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control," Shutt et al., filed on 10/11/2001; 35 pages	<input type="checkbox"/>
20	USPTO Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm," Warren Snyder, filed on 10/09/2001; 26 pages	<input type="checkbox"/>
21	The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; Snyder et al., filed on 10/26/2000; 277 pages	<input type="checkbox"/>
22	USPTO Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode," Warren Snyder, filed on 10/05/2001; 30 pages	<input type="checkbox"/>

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23	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
25	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Sullam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>
26	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>

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1	Wikipedia.org; "Von Neumann architecture"; retrieved from <a href="http://en.wikipedia.org/wiki/Von_Neumann_architecture">http://en.wikipedia.org/wiki/Von_Neumann_architecture</a> on January 22, 2007; 4 pages	<input type="checkbox"/>
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	1	6107769	B1	2000-08-22	Saylor et al.	Entire Document
	2	7073158	B1	2006-07-04	McCubrey	Entire Document
	3	6642857	B1	2003-11-04	Schediwy et al.	Entire Document
	4	6263339	B1	2001-07-17	Hirsch	Entire Document
	5	6282547	B1	2001-08-28	Hirsch	Entire Document
	6	6678877	B1	2004-01-13	Perry, et al.	Entire Document
	7	5889236	B1	1999-03-30	Gillespie et al.	Entire Document
	8	6661288	B1	2003-12-09	Morgan et al.	Entire Document

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9	6061511	B1	2000-05-09	Marantz et al.	Entire Document
10	6211708	B1	2001-04-03	Klemmer	Entire Document
11	6204687	B1	2001-03-20	Schutiz et al.	Entire Document
12	6850554	B1	2005-02-01	Sha	Entire Document
13	7138841	B1	2006-11-21	Li	Entire Document
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15	5952888	B1	1999-09-14	Scott	Entire Document
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18	7138868	B1	2006-11-21	Sanchez et al.	Entire Document
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21	5861583	B1	1999-01-19	Schediwy et al.	Entire Document
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23	6289489	B1	2001-09-11	Bold et al.	Entire Document
24	5276890	B1	1994-01-04	Arai, Makoto	Entire Document
25	6691301	B1	2004-02-10	Bowen	Entire Document
26	5966532	B1	1999-10-12	McDonald et al.	Entire Document
27	6311149	B1	2001-10-30	Ryan et al.	Entire Document
28	6388109	B1	2002-05-14	Schwarz et al.	Entire Document
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32	6799198	B1	2004-09-28	Huboi et al.	Entire Document
33	7152027	B1	2006-12-19	Adreade et al.	Entire Document
34	3600690	B1	1971-08-17	White, Colin Graham	Entire Document
35	4380083	B1	1983-04-12	Andersson et al.	Entire Document
36	5917356	B1	1999-06-29	Casal et al.	Entire Document
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39	5651035	B1	1997-07-22	Tozun	Entire Document
40	5886582	B1	1999-03-23	Stansell	Entire Document
41	5952888	B1	1999-09-14	Scott	Entire Document

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42	5331315	B1	1994-07-19	Crosetto	Entire Document
43	6888453	B1	2005-05-03	Lutz et al.	Entire Document
44	6172571	B1	2001-01-09	Moyal	Entire Document

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	1	20030126947	A1	2003-07-10	Margaria	Entire Document

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	1	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~staffann/developer/emulator.htm">http://www.algonet.se/~staffann/developer/emulator.htm</a> ; 6 pages	<input type="checkbox"/>

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2	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
3	USPTO Application Number 09/975,105 (CD00184): "Host to FPGA Interface in an In-Circuit Emulation System," Craig Nemecek, filed on 10/10/2001; 44 pages	<input type="checkbox"/>

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	2	7023215	B1	2006-04-04	Steenwyk	Entire Document
	3	7026861	B1	2006-04-11	Steenwyk	Entire Document
	4	7466307	B1	2008-12-16	Trent Jr. et al.	Entire Document
	5	5781747	B1	1998-07-14	Smith et al.	Entire Document
	6	5805792	B1	1998-09-08	Swoboda et al.	Entire Document
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11	5889936	B1	1999-03-30	Chan	Entire Document
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14	5614861	B1	1997-03-25	Harada, Hiroyuki	Entire Document
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25	6560306	B1	2003-05-06	Duffy	Entire Document
26	6667642	B1	2003-12-23	Moyal	Entire Document
27	6369660	B1	2002-04-09	Wei	Entire Document
28	6377646	B1	2002-04-23	Sha	Entire Document
29	6445211	B1	2002-09-03	Saripella	Entire Document
30	6553057	B1	2003-04-22	Sha	Entire Document

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Attorney Docket Number	CD01203M

31	6075941	B1	2000-06-13	Itoh et al.	Entire Document
32	6289300	B1	2001-09-11	Brannick et al.	Entire Document
33	6298320	B1	2001-10-02	Buckmaster et al.	Entire Document
34	6032268	B1	2000-02-29	Swoboda et al.	Entire Document
35	6347395	B1	2002-02-12	Payne et al.	Entire Document
36	6366878	B1	2002-04-02	Grunert	Entire Document
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	1	USPTO Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip," Warren Snyder; filed on 10/22/2001; 117 pages	<input type="checkbox"/>
	2	USPTO Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital)," Snyder et al., filed on 03/16/2004; 13 pages	<input type="checkbox"/>

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1	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages DATE	<input type="checkbox"/>
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4	USPTO Application Number 09/721,316 (CD98087C1): "Programmable Oscillator Scheme," Mar et al., filed on 11/22/2000; 26 pages	<input type="checkbox"/>
5	USPTO Application Number 10/324,455 (CD98087C2): "Programmable Oscillator Scheme," Mar et al., filed on 12/20/2002; 23 pages	<input type="checkbox"/>
6	USPTO Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
7	USPTO Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State," Craig Nemecek, filed on 11/15/2001; 33 pages	<input type="checkbox"/>
8	USPTO Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System," Nemecek et al., filed on 03/29/2002; 32 pages	<input type="checkbox"/>
9	USPTO Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user interface for Programming an Electronic Device," Bartz et al., filed on 11/19/2001; 43 pages	<input type="checkbox"/>
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12	USPTO Application Number 09/404,891 (CD99054): "Method, Architecture and Circuitry for Controlling Pulse Width in a Phase and/or Frequency Detector," Scott et al., filed on 09/24/1999; 17 pages	<input type="checkbox"/>
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3	USPTO Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Paul H. Scott, filed on 03/29/1998; 35 pages	<input type="checkbox"/>
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7	USPTO Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al., filed on 02/26/2002; 28 pages	<input type="checkbox"/>
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9	USPTO Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Michael T. Moore, filed on 06/27/2001; 32 pages	<input type="checkbox"/>
10	USPTO Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al., filed on 08/24/2004; 24 pages	<input type="checkbox"/>
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12	USPTO Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al., filed on 12/22/2000; 9 pages	<input type="checkbox"/>
13	USPTO Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Lane T. Hauck, filed on 10/17/2001; 28 pages	<input type="checkbox"/>
14	USPTO Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Rengarajan S. Krishnan, filed on 03/30/2000; 27 pages	<input type="checkbox"/>
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9	USPTO Application Number 09/989,761 (CD01179M): "Storing of global parameter defaults and using them over two or more design projects," Ogami et al., filed on 11/19/2001; 37 pages	<input type="checkbox"/>
10	Burogs et al., "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics, 1998, Vol. 2; 6 pages	<input type="checkbox"/>
11	Efstathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," < <a href="http://web.archive.org/web/20021231045232">http://web.archive.org/web/20021231045232</a> > December 31, 2002, version, retrieved from the Internet Archives; 10 pages	<input type="checkbox"/>

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5	Sedra et al., "Microelectronic Circuits," 3rd Edition, Oxford University Press, 02/05/2007; 20 pages	<input type="checkbox"/>
6	USPTO Application Number 11/166,622 (CD05027): "Touch wake for electronic devices," Beard et al., filed on 06/23/2005; 22 pages	<input type="checkbox"/>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10001477
	Filing Date		2001-11-01
	First Named Inventor	Steve Roe	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01203M	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	2	6149299	B1	2000-11-21	Asian et al.	Entire Document
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**U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20080095213	A1	2008-04-24	Lin et al.	Entire Document

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**FOREIGN PATENT DOCUMENTS**

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	1	USPTO Non-Final Rejection for Application Number 11/865,672 (CD2007098) dated 07/17/2009; 6 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/859,547 (CD2007112) dated 10/01/2009; 9 pages	<input type="checkbox"/>
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9	Written Opinion of the International Searching Authority for International Application Number PCT/US08/60695 (CD2007132) dated 07/22/2009; 6 pages	<input type="checkbox"/>
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11	Catthoor et al., "Architectural Strategies for an Application-Specific Synchronous Multiprocessor Environment," IEEE transactions on Acoustics, Speech, and Signal Processing; Vol. 36, No. 2, Feb. 1988, pp. 265-284; 20 pages	<input type="checkbox"/>
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